File   View   Edit   Tools   Window   Help

Active
- L1: (3) (("5884092") or ("6668266")).PN.
- L2: (12) ("4841468" | "4912636" | "5363322" | "5379394" | "5507000" | "5521856" | "5537601" | "5...
- L3: (7) ("4912636" | "5379394" | "5507000" | "5537601" | "5588118" | "5590357" | "5630153").PN.
- L4: (12) 2 3
- L5: (2) ("6434690").PN.
- L6: (8) ("4862407" | "5361373" | "5588118" | "5630153" | "5740461" | "5832248" | "5832258" | "58...
- L7: (27681) (fixed fix) adj point
- L8: (344423) integer$1
- L10: (335328) processing adj (element$1 or unit$1) or pe or pu
- L11: (365746) dsp or 10
- L13: (1020) 7 with 8
- L14: (37) 11 with 13
- L15: (768164) fraction$2 or mantissa or floating
- L16: (13799) 8 with 15
- L17: (352) 11 with 16
- L18: (356346) ALU or cpu
- L21: (37118) 18 with 11 with control$4
- L22: (38) 17 and 21
- L23: (258016) multiplier or multiplication or multiplying
- L24: (815) 23 with 16
- L26: (4) 21 same 24
- L27: (49) 23 with 13
- L28: (0) 21 same 27
- L30: (9) 27 same 11

Failed
- (0) '

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DBs   USPAT US-PGPUB; EPO,.   Plurals
Default operator,   OR   Highlight all hit terms initially

BRS term   KWR form   Image   Text   HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P	2	3			In
1			US 5630153 A	19970513	17	Integrated digital signal processor/general purpose CPU	712/35	710/110; 712/36		Intrater, Amos et al.								US
2			US 5832248 A	19981103	8	Semiconductor integrated circuit having CPU and multiplier	712/200	712/221; 712/34		Kishi, Kazumasa et al.								US
3			US 5379394 A	19950103	10	Microprocessor with two groups of internal buses	710/305	711/123; 711/202;		Goto, Harutaka								US

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